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Dr. Geetanjali Mahamunkar

डॉ. गीतांजली महामुणकर

Controller of Examination (I/c)

परिक्षा नियंत्रक (प्र.)

Ref no: DBATU/Suppli.Exam/B.Tech S-26/TT/2026/3280

Date: 01/07/2026

B.Tech Time Table Supplementary Summer-26
VLSI Design & Technology (11378)

Sr. No.	Date	Time	Course Name	Subject Code	Subject Name
1	09-07-2026	10:30 AM TO 01:30 PM	VLSI Design & Technology	BTBTC401	System Design through Verilog
2	13-07-2026	10:30 AM TO 01:30 PM	VLSI Design & Technology	BTBTC402	Microcontrollers and Computer Architecture
3	15-07-2026	10:30 AM TO 01:30 PM	VLSI Design & Technology	BTBTC403	Analog and Digital Communication
4	17-07-2026	10:30 AM TO 01:30 PM	VLSI Design & Technology	BTBTC404	Introduction to VLSI lifecycle
5	20-07-2026	10:30 AM TO 01:30 PM	VLSI Design & Technology	BTVTPE405A	Probability Theory and Random Processes
				BTVTPE405B	Analog Circuits
				BTVTPE405C	Problem solving through programming in C
				BTVTPE405D	Object oriented programming through C++

Important Note:

1. The candidates appearing for the examination should report 20 minutes before the start of examination.
2. Mobile phones and other electronic gadgets are prohibited in the examination hall.

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